

Notice of References Cited	Application/Control No. 10/750,714		Applicant(s)/Patent Under Reexamination GONZALEZ ET AL.	
	Examiner Benjamin P. Geib		Art Unit 2181	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-5,847,578	12-1998	Noakes et al.	326/39
*	B	US-6,415,424	07-2002	Arimilli et al.	716/8
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
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	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	"A Virtual Bus Architecture for Dynamic Parallel Processing"; K. C. Lee; 1993; IEEE
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	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.